



CPU-GPU tracing using ROCm

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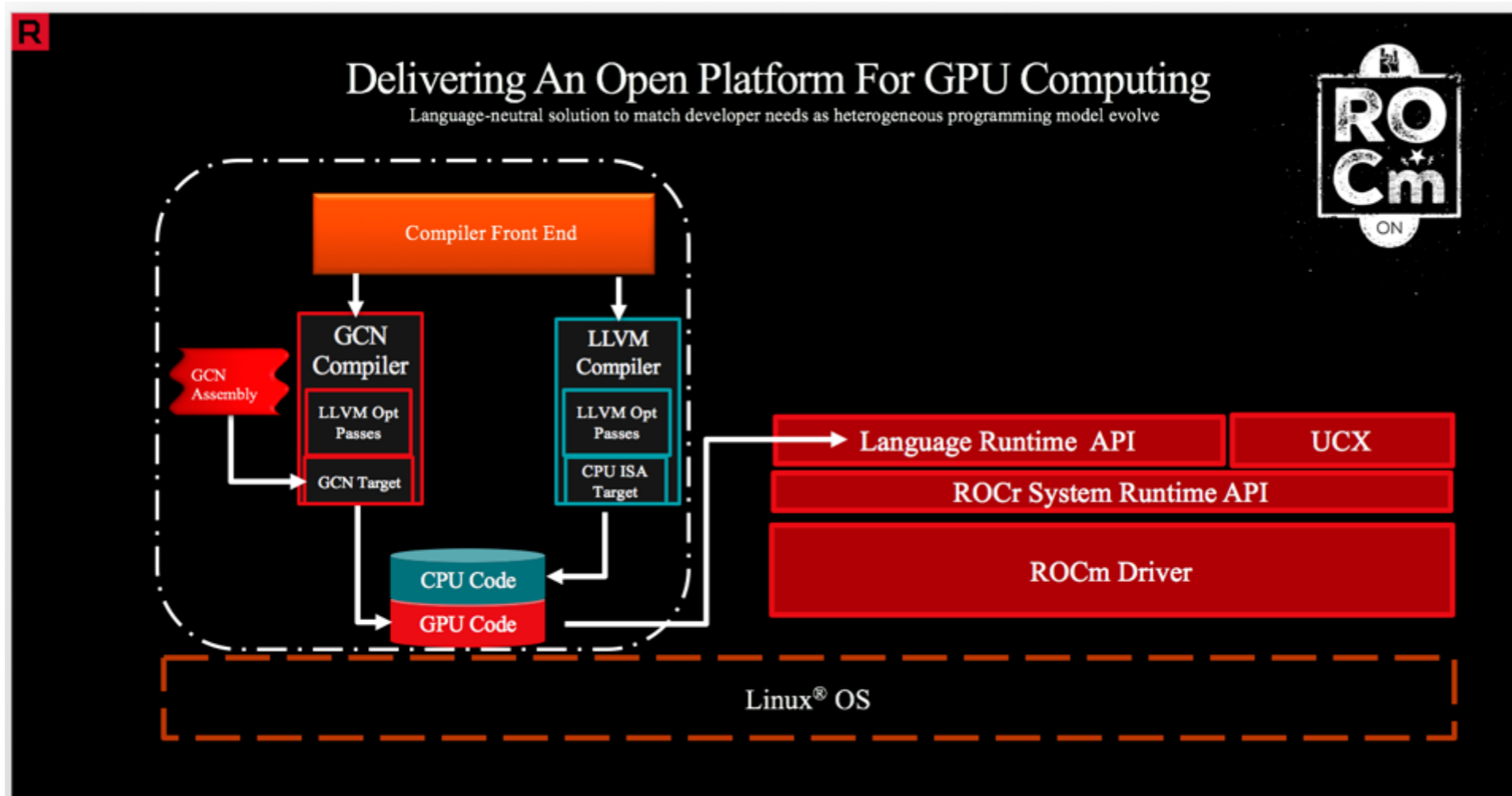
Polytechnique Montreal
DORSAL Laboratory

Agenda

- Background
- Tracing pipeline
- Results
- Future Work



Background



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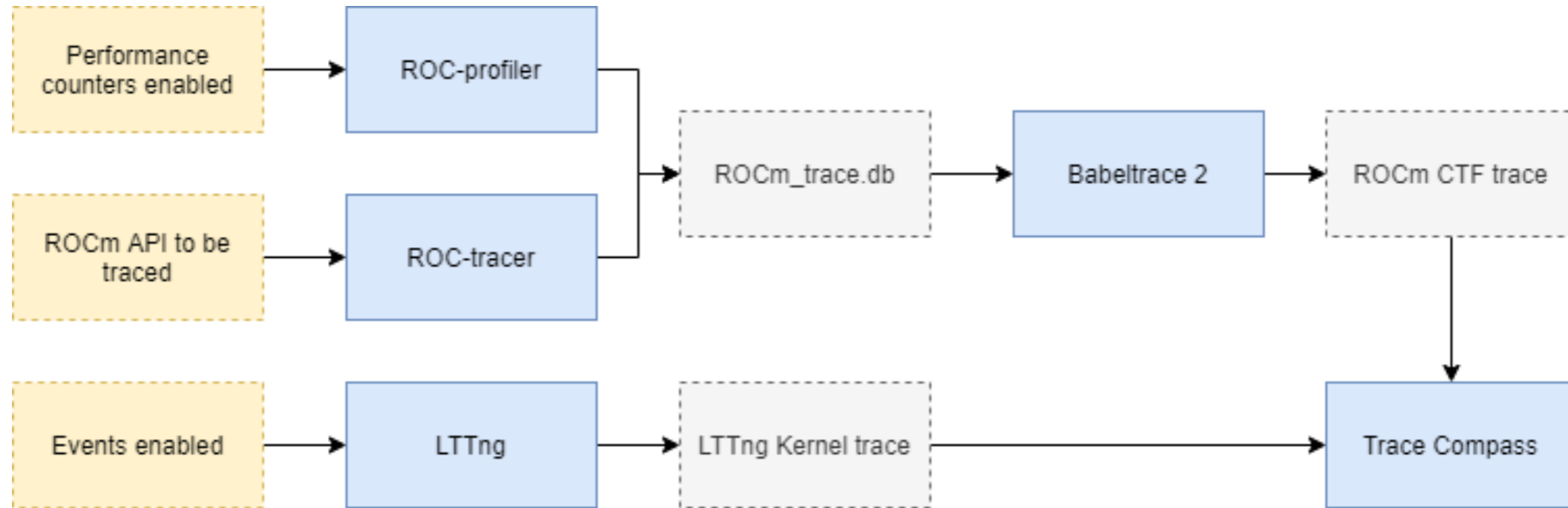


Background

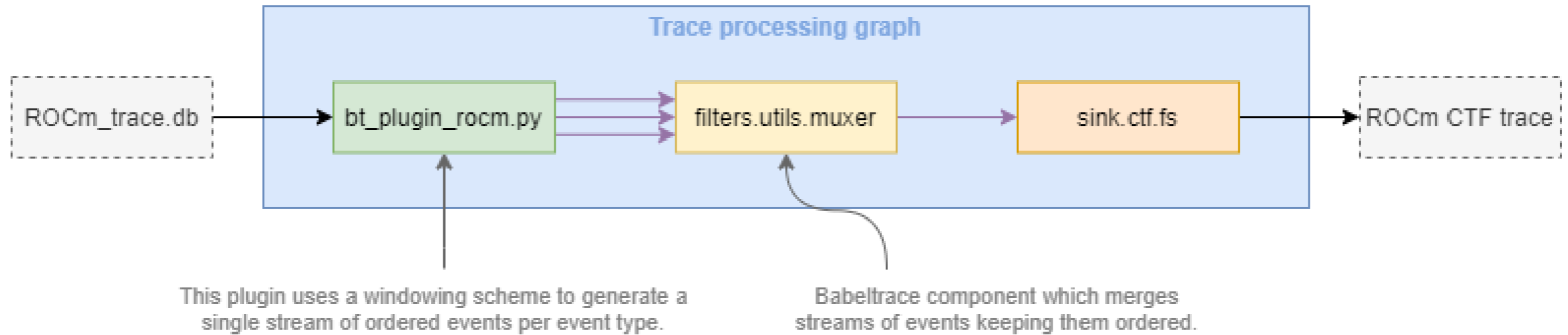
- ROCm works with OpenMP, OpenCL and HIP.
- CUDA code can be easily converted to HIP.
- It is compatible with Deep Learning libraries like Pytorch, Tensorflow and others.
- ROC-profiler and ROC-tracer can trace multiple APIs : HIP, HSA, KFD and code annotation (ROCtx).
- ROC-profiler also provides performance counters.



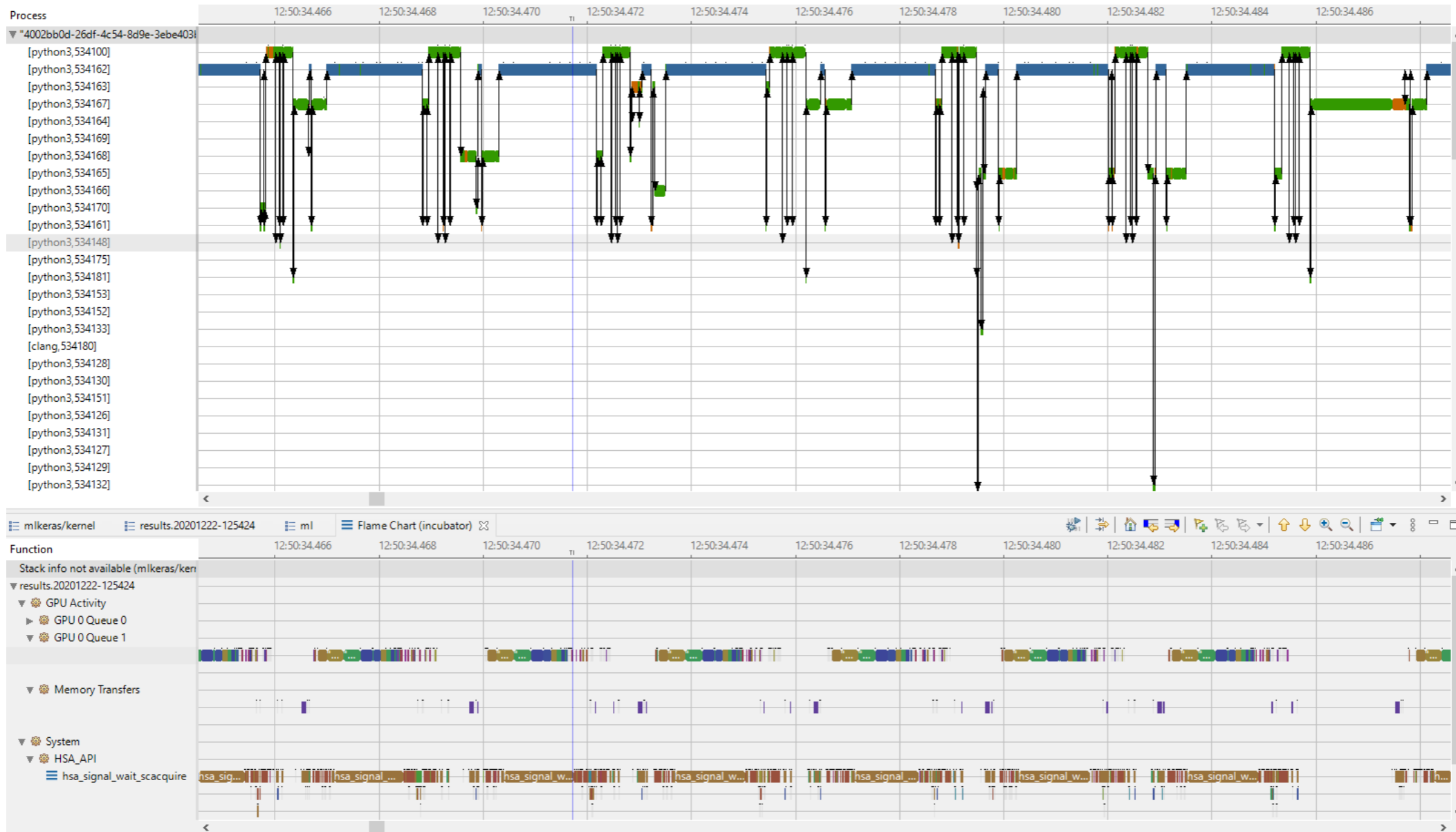
Tracing pipeline



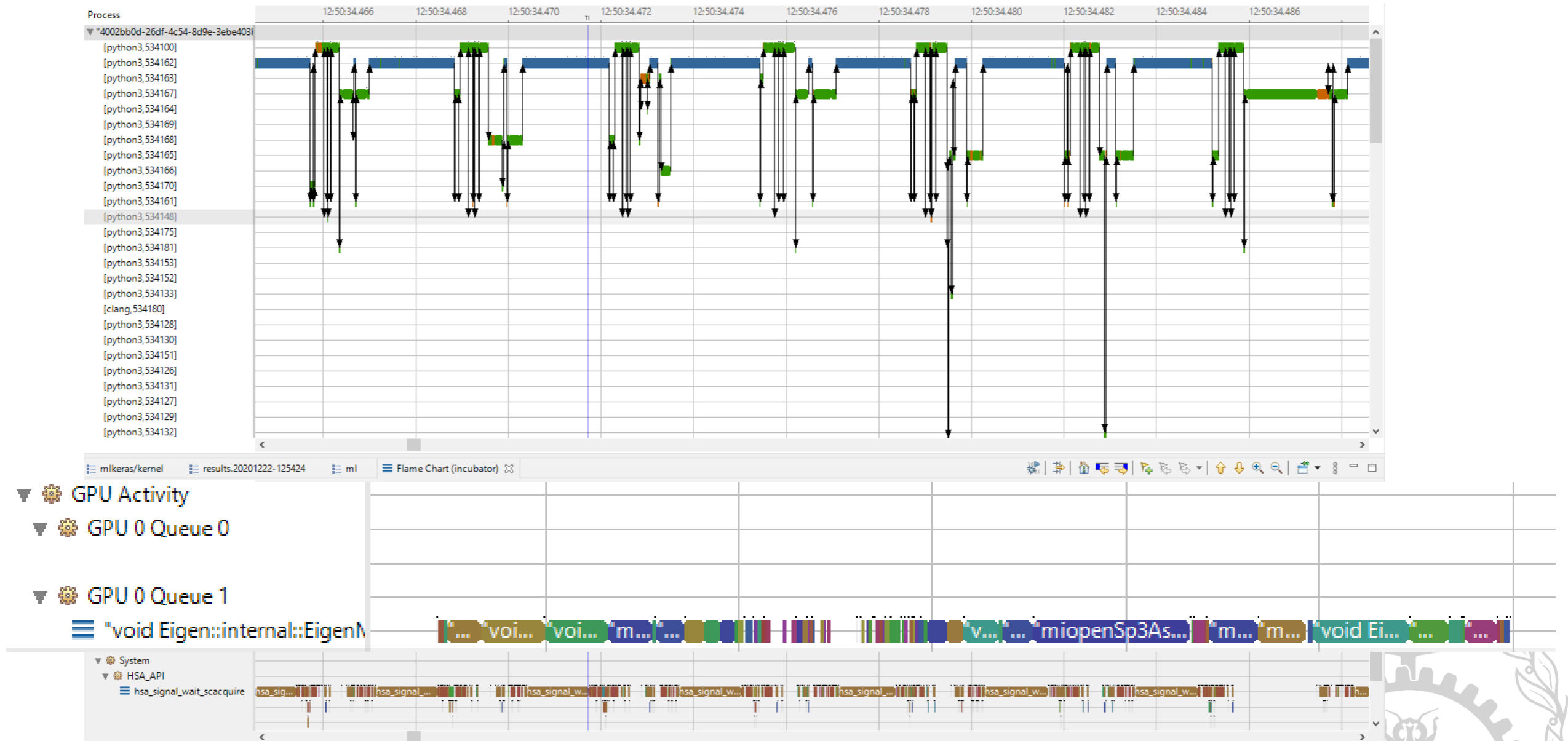
Tracing pipeline



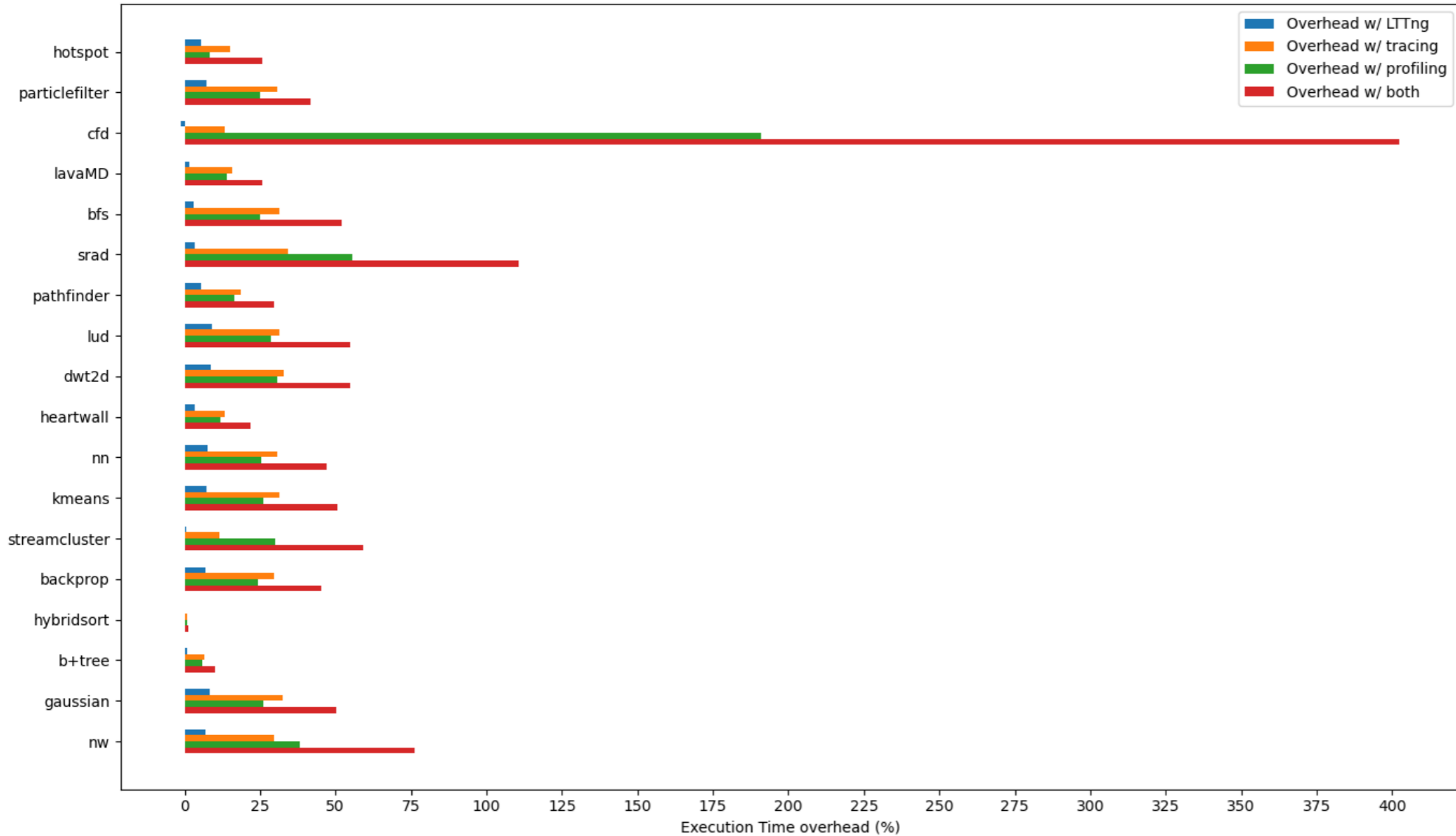
Results



Results



Results



The experiences are run with LTTng and only enabling the HSA API.



Results

Experience	Number of kernels	Number of events	Trace Size
nn	1	8377	1.23 MB
hotspot	1	8413	1.24 MB
lavaMD	1	8434	1.24 MB
pathfinder	5	8641	1.24 MB
gaussian	4	8632	1.25 MB
backprop	2	8548	1.25 MB
b+tree	2	8725	1.27 MB
lud	46	10946	1.28 MB
dwt2d	27	10045	1.29 MB
kmeans	9	9220	1.29 MB
bfs	16	9603	1.30 MB
particlefilter	9	9894	1.38 MB
hybridsort	120	15934	1.49 MB
nw	255	22878	1.54 MB
heartwall	20	11265	1.54 MB
srad	502	40497	2.32 MB
cfid	16004	932665	23.25 MB
streamcluster	5549	456819	26.34 MB

The number of events is recorded for the ROCm trace whereas trace size takes into account the ROCm trace as well as the kernel trace.



Results

- The trace shown had a size of 1.72 GB.
- The trace size varies a lot depending on what API is traced.
- In particular, KFD events are very frequent



Future Work

- Synchronization of ROCm traces between different nodes
- Support for HIP streams
- Support for MPI programs
- And more...

